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ZILKA·KOTAB

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June 12, 2007

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Title: On-The-Fly Redundancy Operation For Forming Redundant Drive Data And Reconstructing Missing Data As Data Transferred Between Buffer Memory And Disk Drives During Write And Read Operation Respectively

Serial No. 09/411,698 Patent No. 6,237,052 Filing date: 10/1/1999 Issue date: 5/22/2001

Dear Sir:

Please enter the enclosed Revocation and Power of Attorney and Statement Under 37 CFR 3.73(b) forms into the file of the referenced application.

Very truly yours, Zilka-Kotab, PC

/KEVINZILKA/

Kevin J. Zilka Reg. No. 41,429

Enclosure

CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being facsimile transmitted to the Commissioner of Patents at facsimile number: (571)273-8300 on June 12, 2007.

Erica L. Farlow

Date

-1-IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Multi-Platter Disk Drive Controller and Methods for Synchronous Redundant Data

Operations

Serial No. 11/266,764 Filing date: 11/02/2005

Title: Intelligent Storage Engine for Disk Drive Operations with Reduced Local Bus Traffic

Serial No. 11/316,436 Filing date: 12/22/2005

Title: Disk Controller Methods and Apparatus with Improved Striping, Redundancy Operations

and Interfaces

Serial No. 11/080,376 Filing date: 03/14/2005

Title: Disk Array Controller with Reconfigurable Data Path

Serial No. 10/829,918 Filing date: 04/21/2004

Title: Method and Apparatus for Synchronizing Data from Asynchronous Disk Drive Data

Transfers

Serial No. 10/822,115 Filing date: 04/08/2004

Title: Raid Controller Disk Write Mask

Serial No. 11/060.884 Filing date: 02/17/2005

Title: Raid Controller Disk Write Mask

Serial No. 10/267,928

Patent No. 6,904,498

Filing date: 10/08/2002

Issue date: 06/07/2005

Title: ATA Emulation Host Interface in a RAID Controller

Serial No. 10/885,960 Filing date: 07/06/2004

Title: Disk Array Controller For Reading/Writing Striped Data Using A Single Address Counter

For Synchronously Transferring Data Between Data Ports And Buffer Memory

Serial No. 08/642,453

Filing date: 5/3/1996

Patent No. 6,018,778

Issue date: 1/25/2000

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ZILKA-KOTAB, PC

JUN 1 2 2007

4ENO. 6355 942 P. 3/4

-2-

Title: On-The-Fly Redundancy Operation For Forming Redundant Drive Data And Reconstructing Missing Data As Data Transferred Between Buffer Memory And Disk Drives

During Write And Read Operation Respectively

Serial No. 09/411,698 Patent No. 6,237,052 Filing date: 10/1/1999 Issue date: 5/22/2001

Title: Raid Controller System and Method With ATA Emulation Host Interface

Serial No. 09/667,965

Filing date: 9/21/2000

Patent No. 6,772,108

Issue date: 8/3/2004

Title: On-The-Fly Redundancy Operation For Forming Redundant Drive Data And

Reconstructing Missing Data As Data Transferred Between Buffer Memory And Disk Drives

During Write And Read Operation Respectively

Serial No. 10/445,396 Filing date: 05/22/2003

REVOCATION OF PRIOR POWERS OF ATTORNEY AND GRANT OF NEW POWER OF ATTORNEY

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

As assignee of record of the entire interest of the above-identified application(s)/patent(s), all powers of attorney previously given are hereby revoked and the following agents are hereby appointed to prosecute and transact all business in the U.S. Patent and Trademark Office connected therewith.

Kevin J. Zilka (Reg. No. 41,429); Dominic M. Kotab (Reg. No. 42,762); Richard B. Domingo (Reg. No. 36,784); Joseph Vo (Reg. No. 43,971); and T. Rao Coca (Reg. No. 29,784).

Please send all correspondence for these application(s)/patent(s) as follows:

Customer Number: 28875
Zilka-Kotab, PC
P.O. Box 721120
San Jose, California 95172-1120

Please direct any calls to Kevin Zilka at (408) 505-5100.

Date <u>5/10/07</u>	Signed by: Richard B. Domingo
	Title: Director or Intellectual Property
	Assignee of Record: NVIDIA Composition

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4ENO. 6355,942P. 4/403

JUN 1 2 2007

PTO/SB/96 (04-07)
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STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner: Michael C. Stolowitz	
Application No./Patent No.: 09/411,698 Filed/Issue Date: 10/01/1999 On-The-Fly Redundancy Operation For Forming Redundant Drive Data And Reconstructing Missing Data	
On-The-Fly Redundancy Operation For Forming Redundant Drive Data And Reconstructing Missing Data Entitled: A Data Transfer of the Property of	
Entitled: As Data Transferred Between Buffer Memory And Disk Drives During Write And Read Operation Respectively	
Nvidia Corporation (Nerro of Assignment Assignment and Assignment	
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an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is%)	
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As required by 37 CFR 3.73(b)(1)(f), the documentary evidence of the chain of title from the original owner to the assigned was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11. [NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]	
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.	
Signature Date	
Richard B. Domingo 409 - 566 - 648 1	
Printed or Typed Name Telephone Number	
Director of Intellectual Property	
Title	

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentially is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Arrocomments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Palent and Trademerk Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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